Debugging Transactional Programs

Using the tm_db library

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- Can be easier than
  debugging general multithreaded programs
  - Isolation: think in pre and post conditions
    ↳ Display *logical values*
  - No deadlocks
  - Simpler programs, simpler debugging
- But, debuggers will have to change

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  - Many different STM algorithms
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- Transactional Memory is still an open research area
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    No clear agreement on "the right choice"
    - Hardware support in the future
    - Do not want to expose it to the user
    - Preferably not to the debugger either
  - Abstraction layer: generic debugging infrastructure for transactional programs

Our Contribution

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1. Interface for *generic transactional debugging support*
   - Abstract away details of TM runtime
   - Help TM designers keep debugging in mind
     - Gives TM runtimes a well-defined interface for transactional debugging

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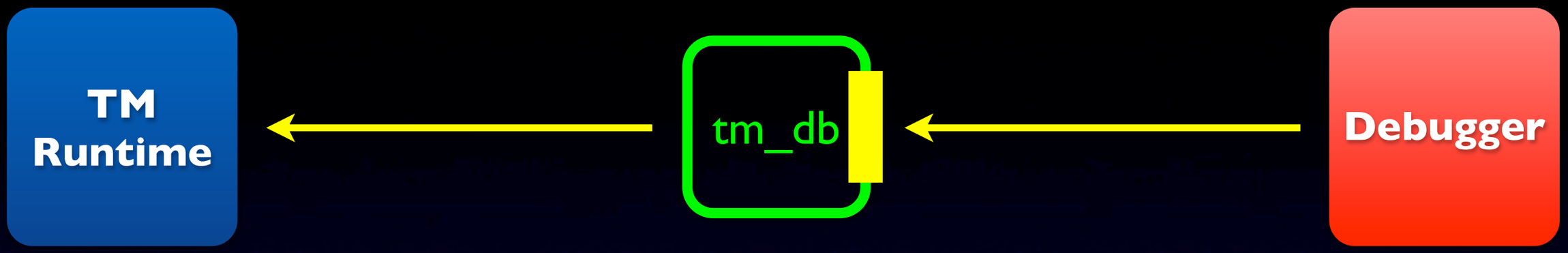

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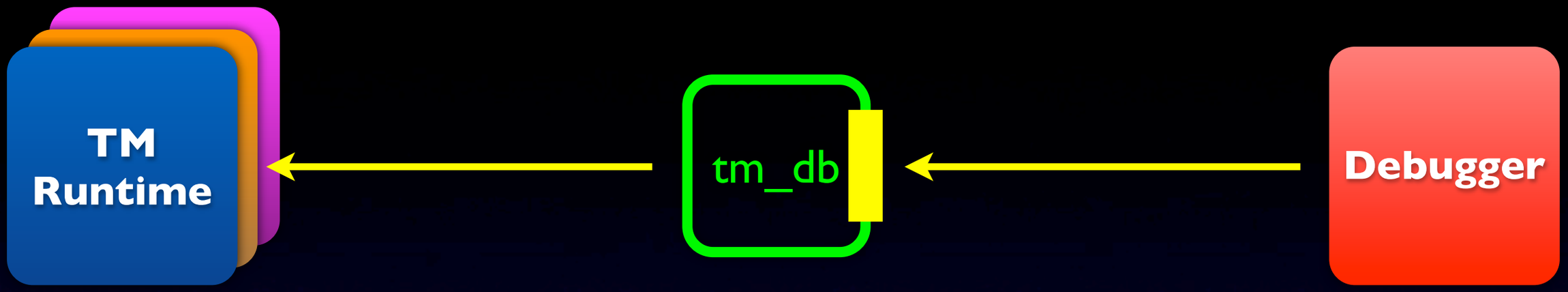

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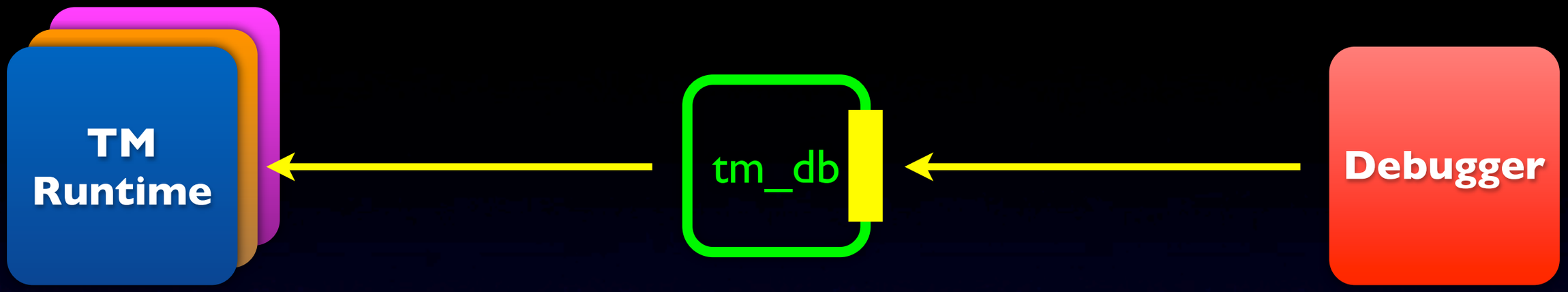

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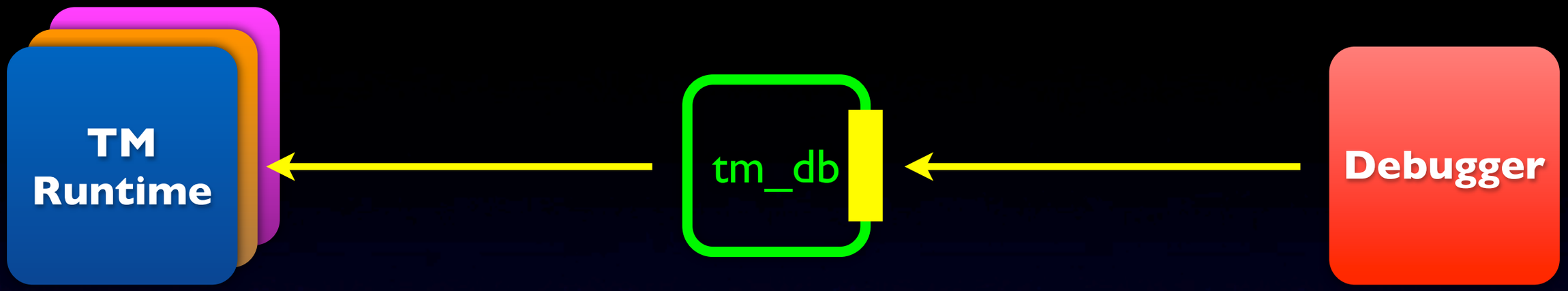
2. The tm_db library: implements this interface
   - Can support multiple TM runtimes
   - Supports multiple debuggers
   - Open source: experiment with new debugging features

Agenda



- "Generic transactional debugging support"
  - What features?
  - In what sense are they generic?
- Library design and implementation

Examining Memory

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  - Examine write set to show tentative values of currently debugged transaction

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- Three notions of interest
  - Atomic block
    lexical scope
  - Logical transaction
    successful execution of an atomic block
  - Physical transaction
    system level: an attempt to execute a logical transaction

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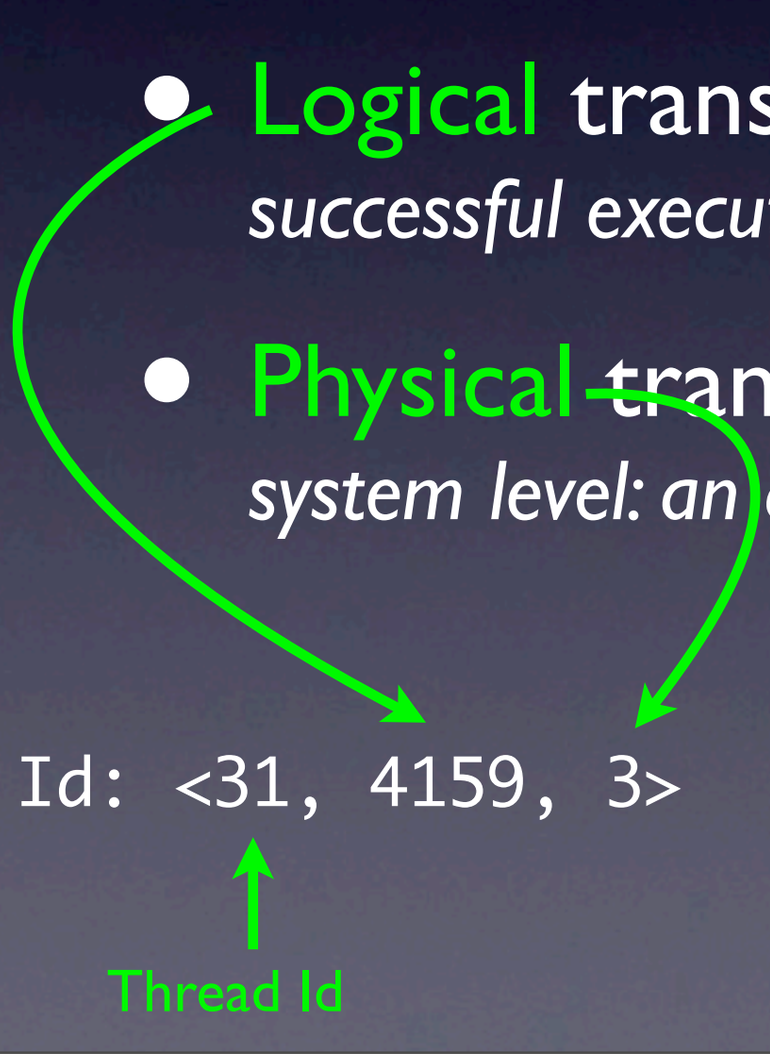

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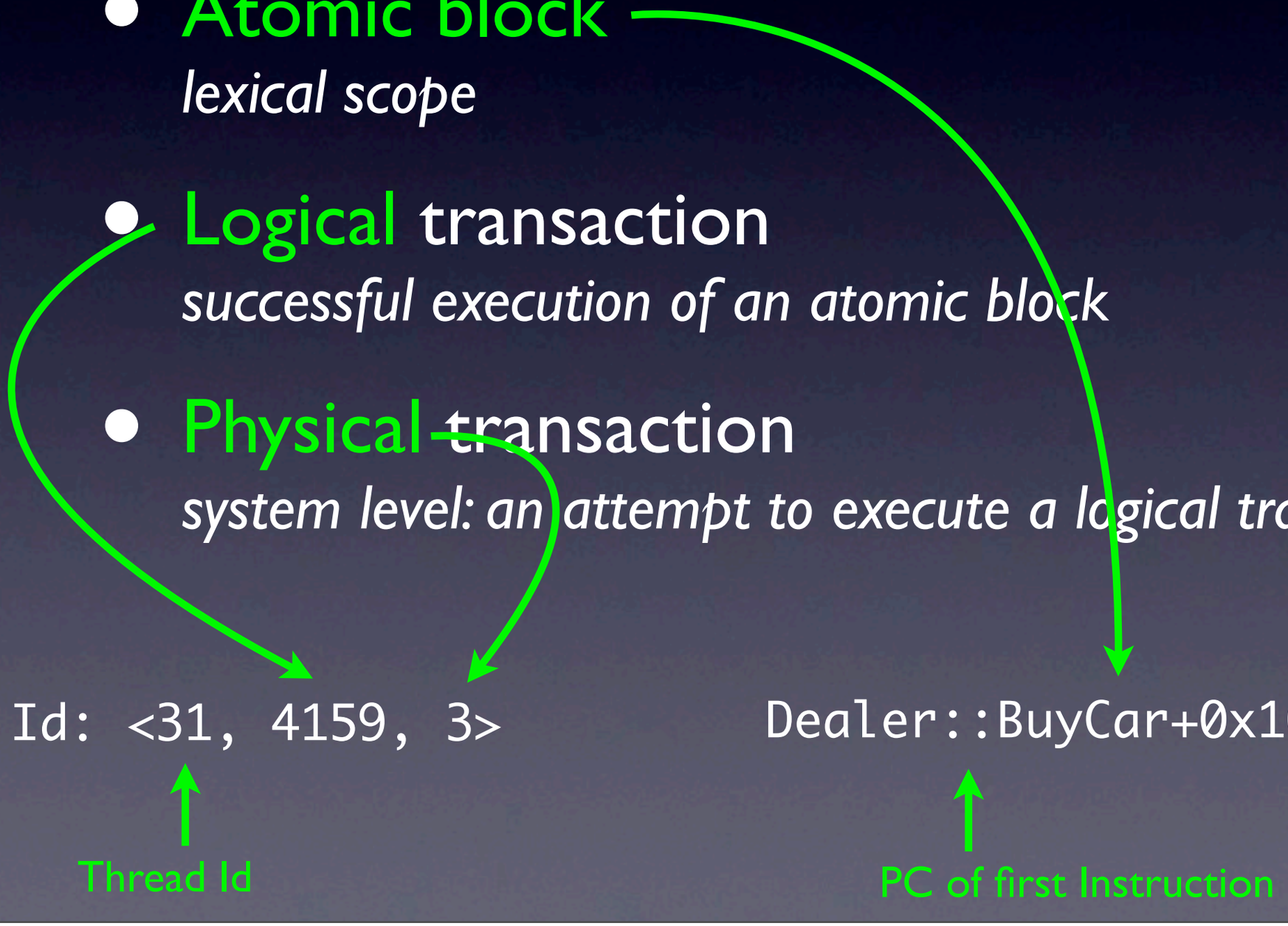

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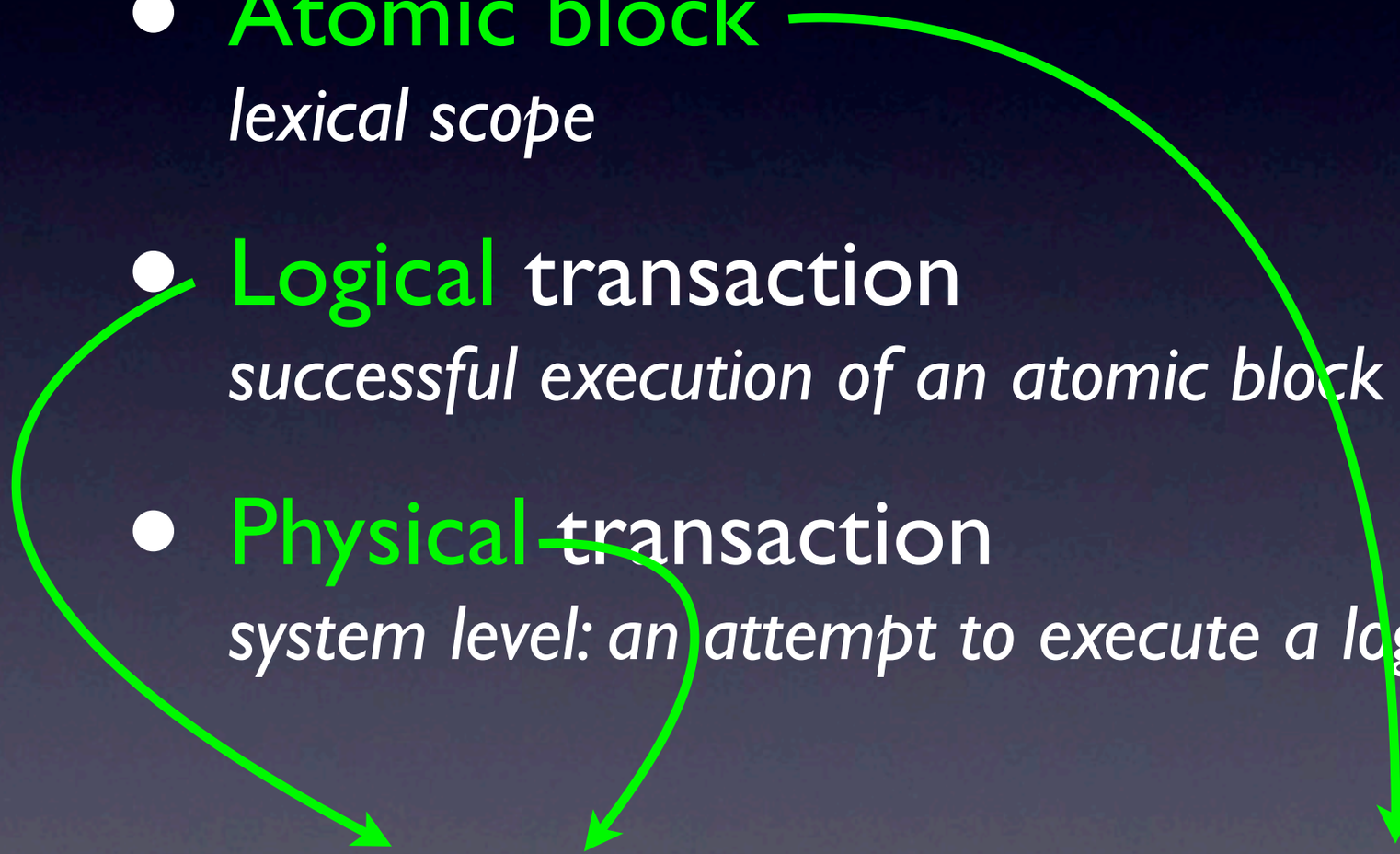

```
Tx Id: <31, 4159, 3>        Dealer::BuyCar+0x10
Tx Id: <31, 4159, 5>        Dealer::BuyCar+0x10
```

Transaction Status

- Tx Status
  Active, Invalid, Aborted, Committed

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Tx Id	Atomic Block	Status
<2,1907,0>	PushHead+0x8	Committed
<3,2217,1>	PushTail+0x8	Invalid
<4,2082,0>	PushHead+0x8	Aborted
<5,2107,0>	PushHead+0x8	Active
<6,2210,0>	PushTail+0x8	Invalid

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- Status query also reports whether completed

Tx Id	Atomic Block	Status	
<2,1907,0>	PushHead+0x8	Committed	(Completed)
<3,2217,2>	PushTail+0x8	Committed	
<4,2082,0>	PushHead+0x8	Aborted	(Completed)
<5,2107,0>	PushHead+0x8	Active	
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Access and Coverage

- Read and Write Accesses:
  examine set of read and written locations

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  examine set of read and written locations
- Not enough due to false conflicts
  - Tx accesses location L
    conflicts with accessed to more than just location L
  - Capture by Coverage

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- Library provides:
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- Library provides:
  - Iterator over accessed locations
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- Find conflicts on a variable

```
> tx coverage &g_list->next->key
```

Tx Id	Atomic Block	Status	Coverage	Read	Written
<2,1907,0>	Insert+0x8	Active	R & W	✓	✓
<3,2217,1>	Insert+0x8	Active	R & W	✓	
<4,2082,0>	Insert+0x8	Active	R & W		

Access and Coverage

- Library provides:
  - Iterator over accessed locations
  - Query coverage
- Reported at user access time
- Show *all* conflicts

```
> tx conflicts
```

Address	Tx Id	Atomic Block	Status	Coverage	Read	Written
0x10019dc48	<2,1907,0>	Insert+0x8	Active	R & W	✓	✓
	<3,2217,1>	Insert+0x8	Active	R & W	✓	
	<4,2082,0>	Insert+0x8	Active	R & W		
0x10018bde0	<2,1907,0>	Insert+0x8	Active	R	✓	
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- Monitoring Scopes

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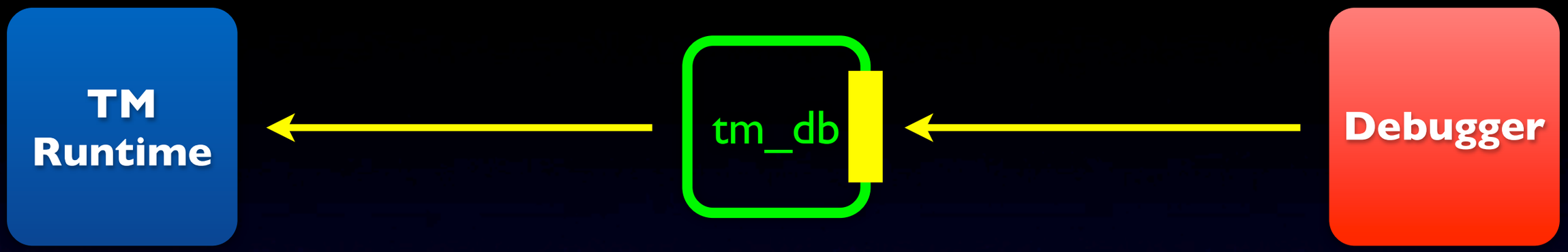

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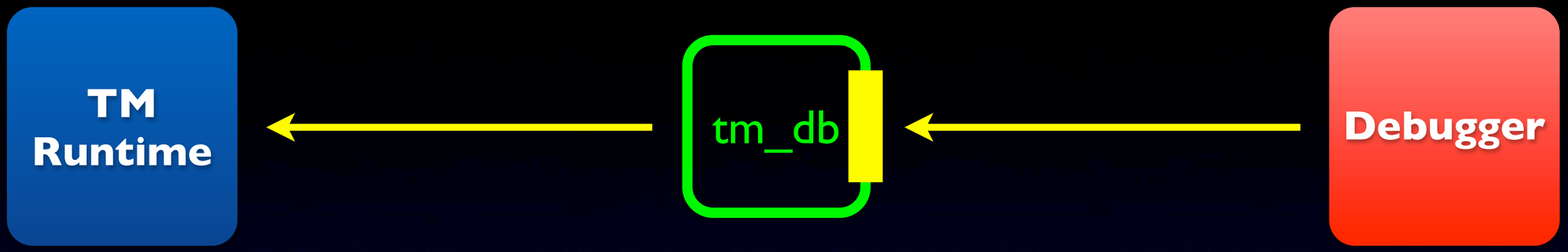

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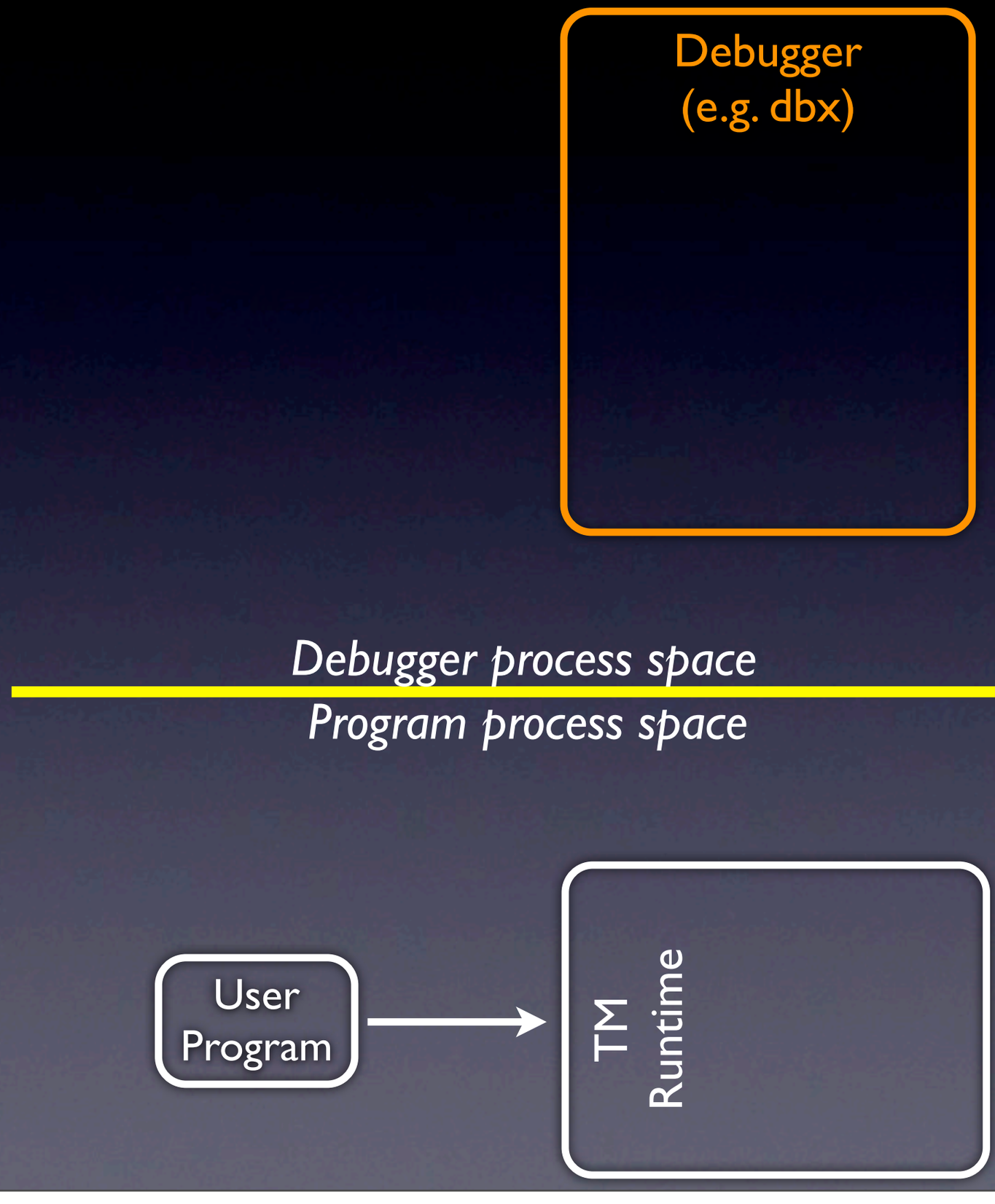
tm_db: Solution Design

- Program and debugger ran by different processes
- proc_service: provides external libraries access to the debugged process
  - mostly read/write remote memory
- RDM:
  Remote Debugging Module
  TM-specific part of the solution



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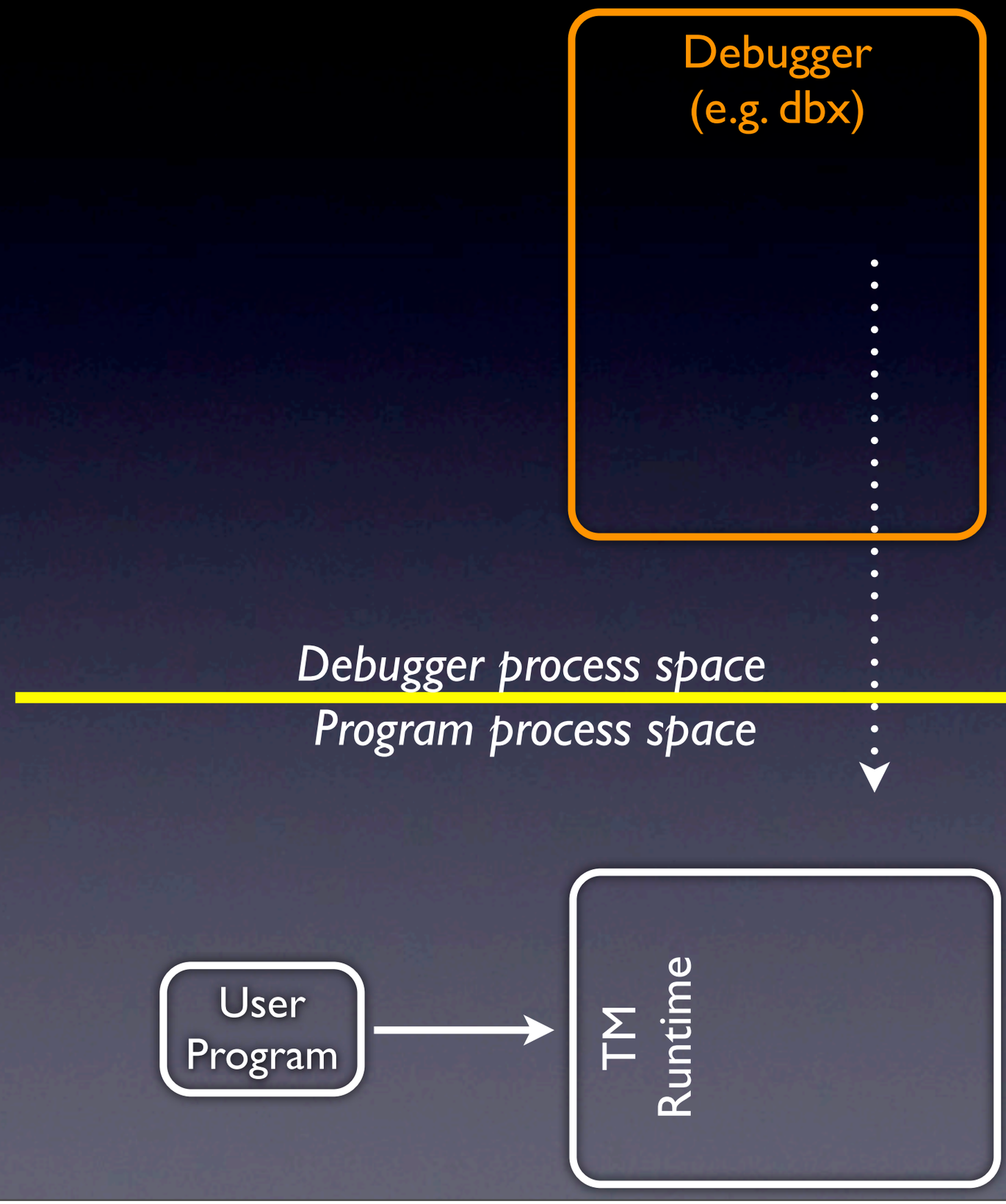

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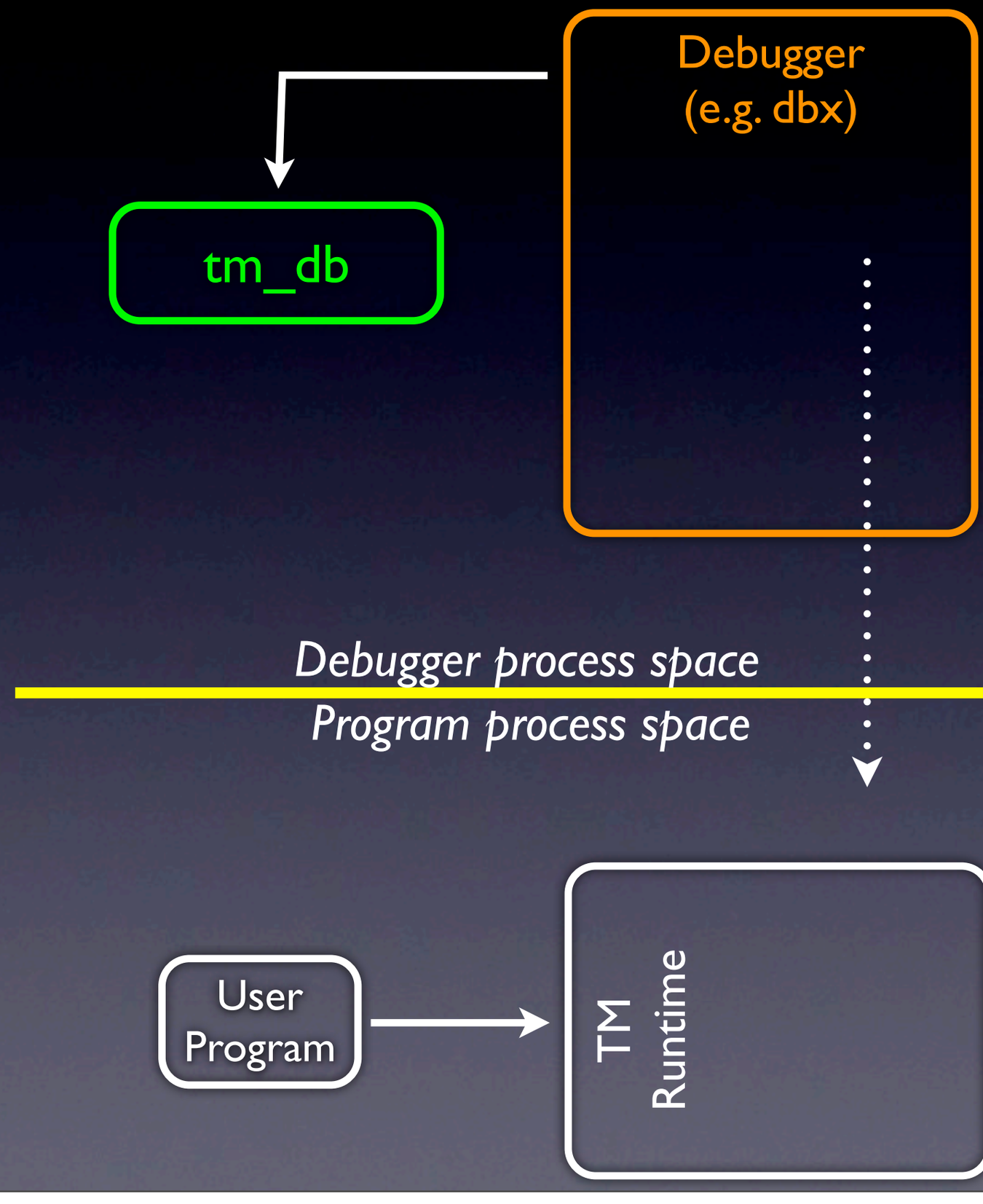

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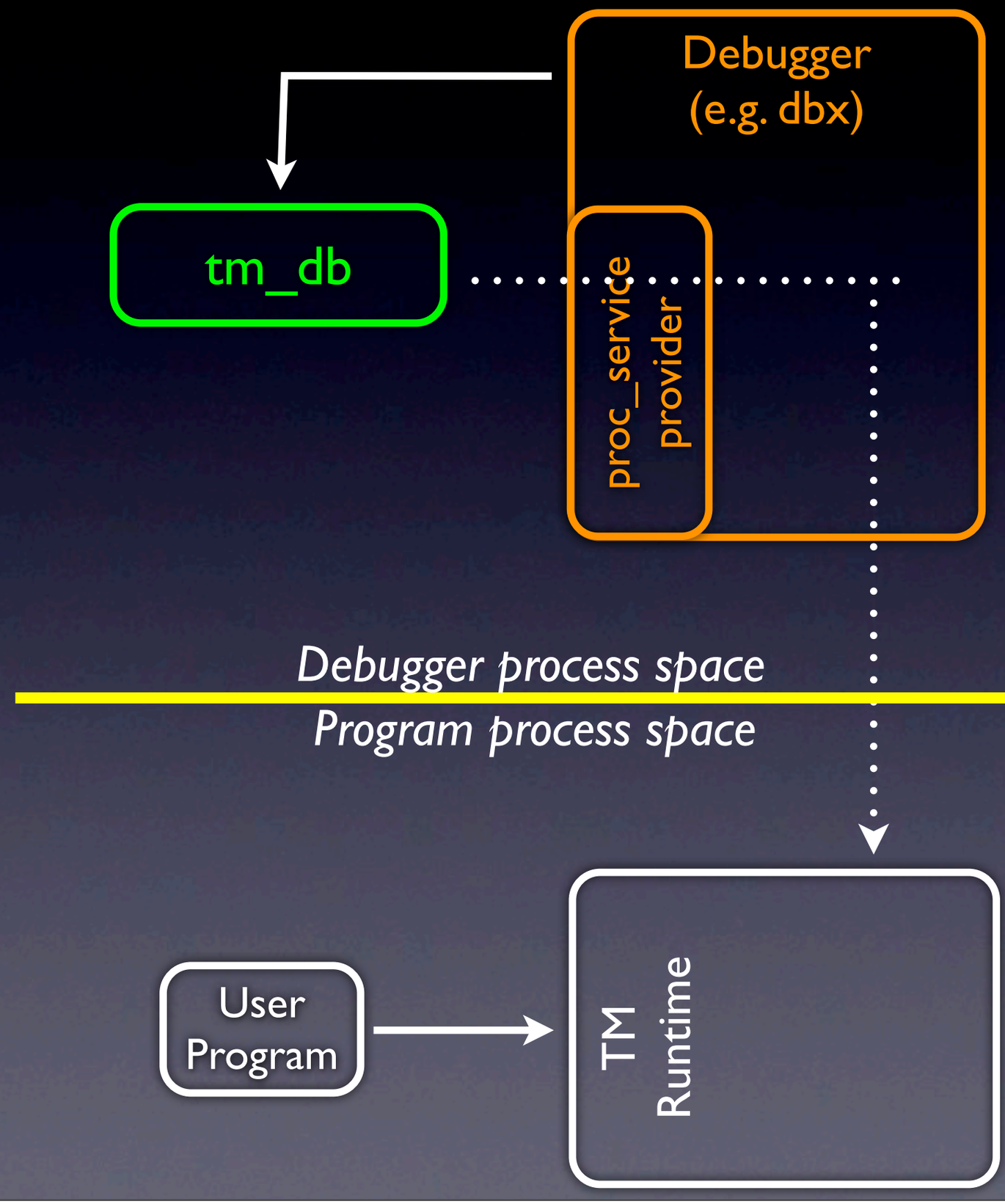

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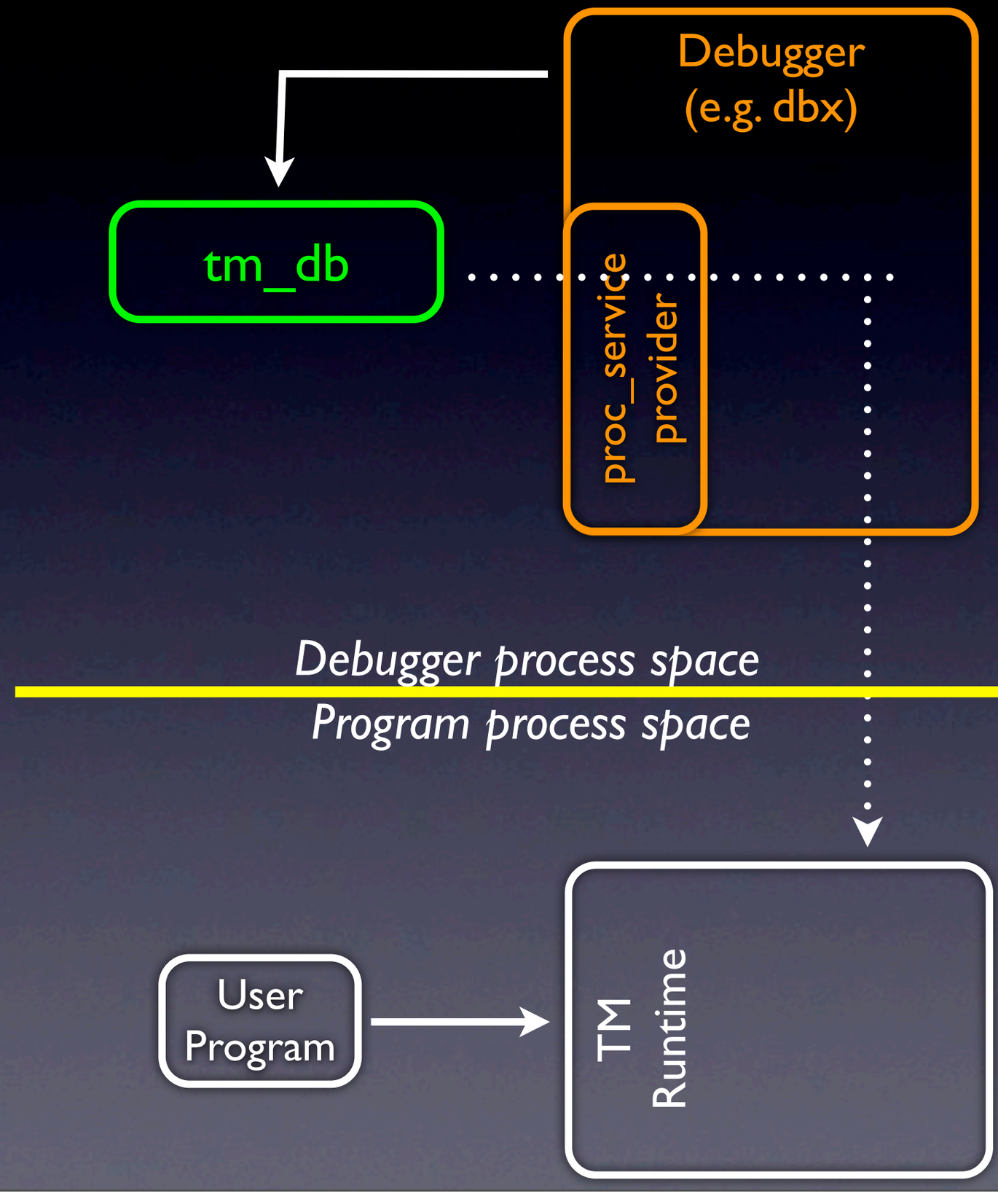

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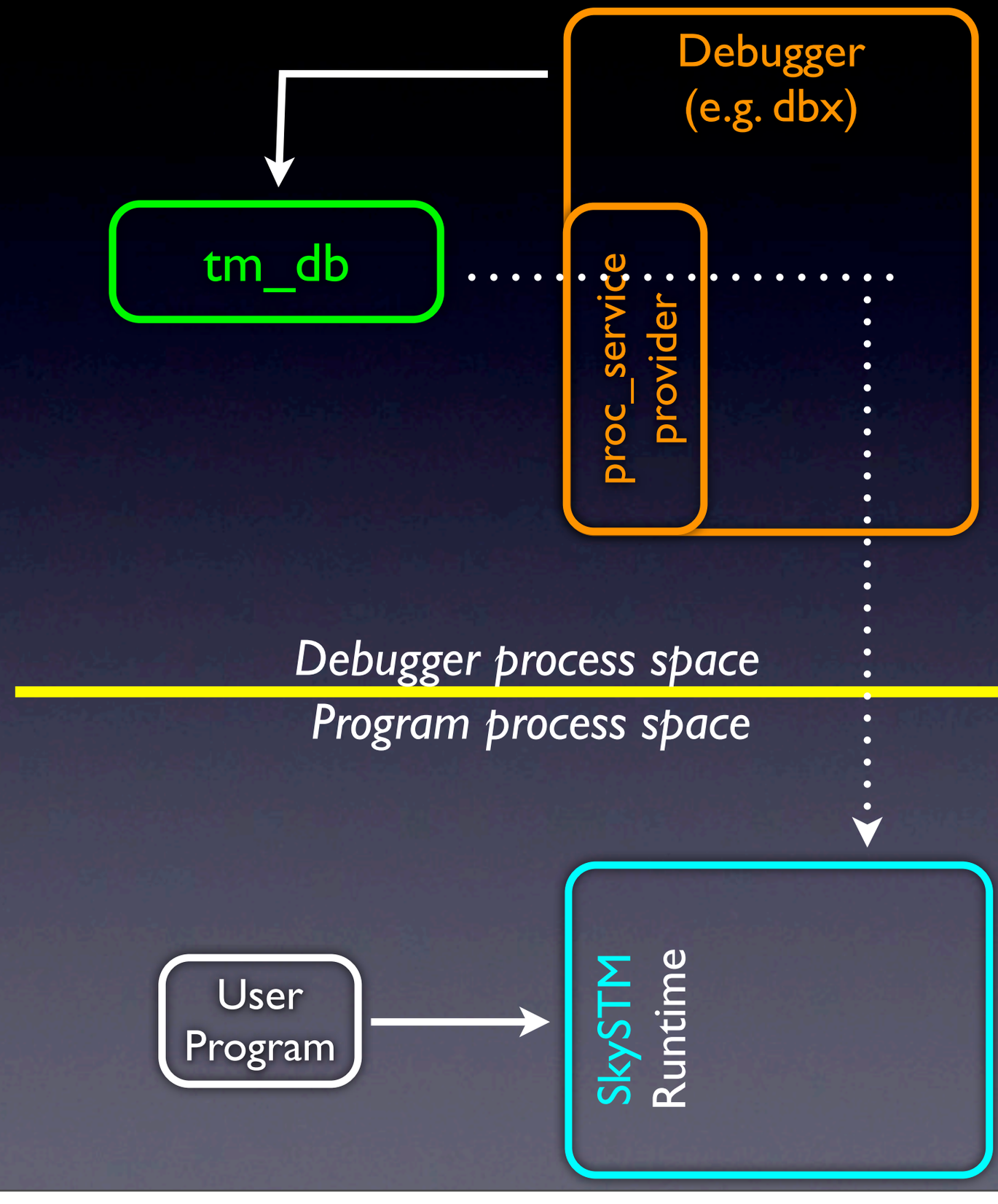

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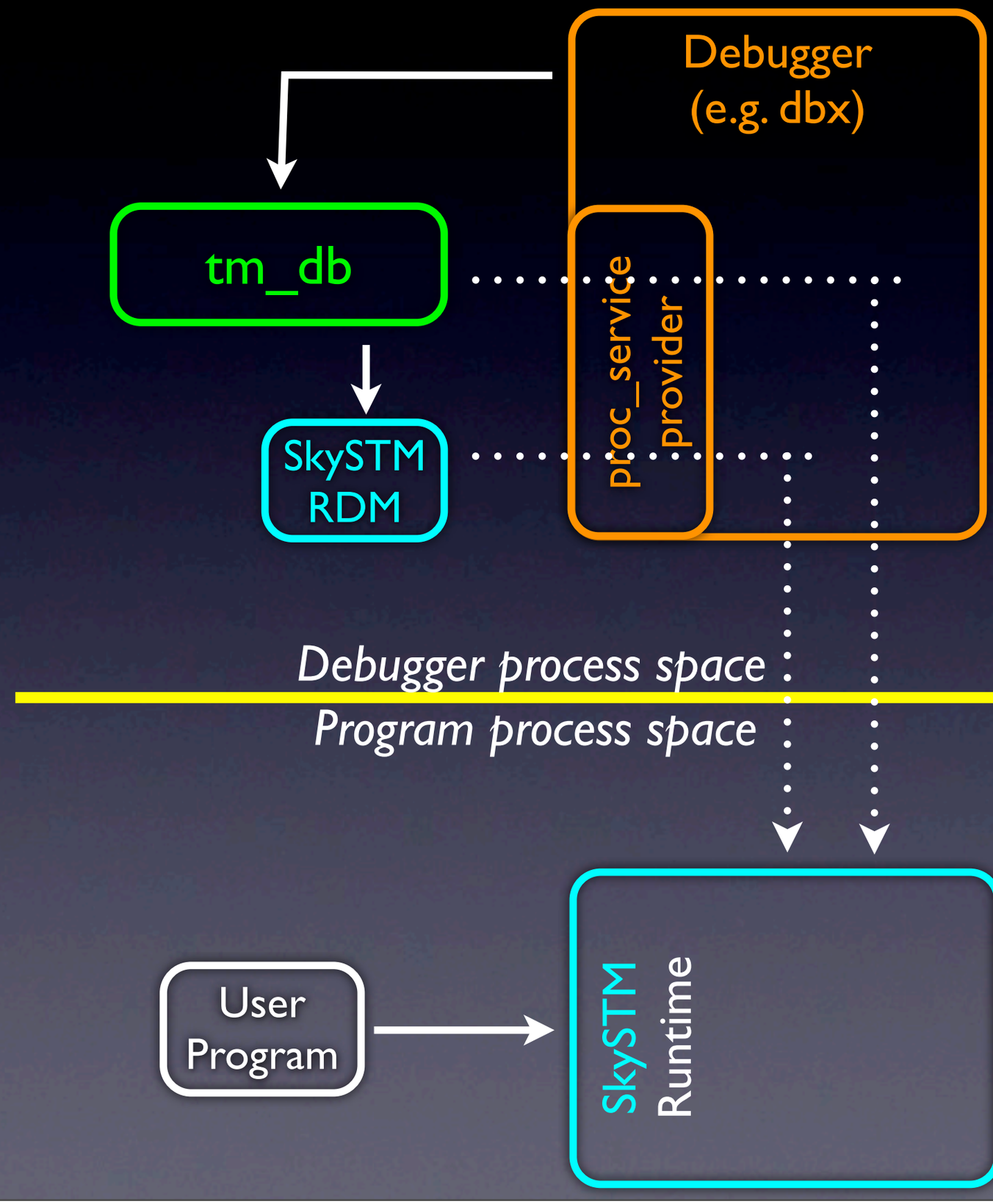

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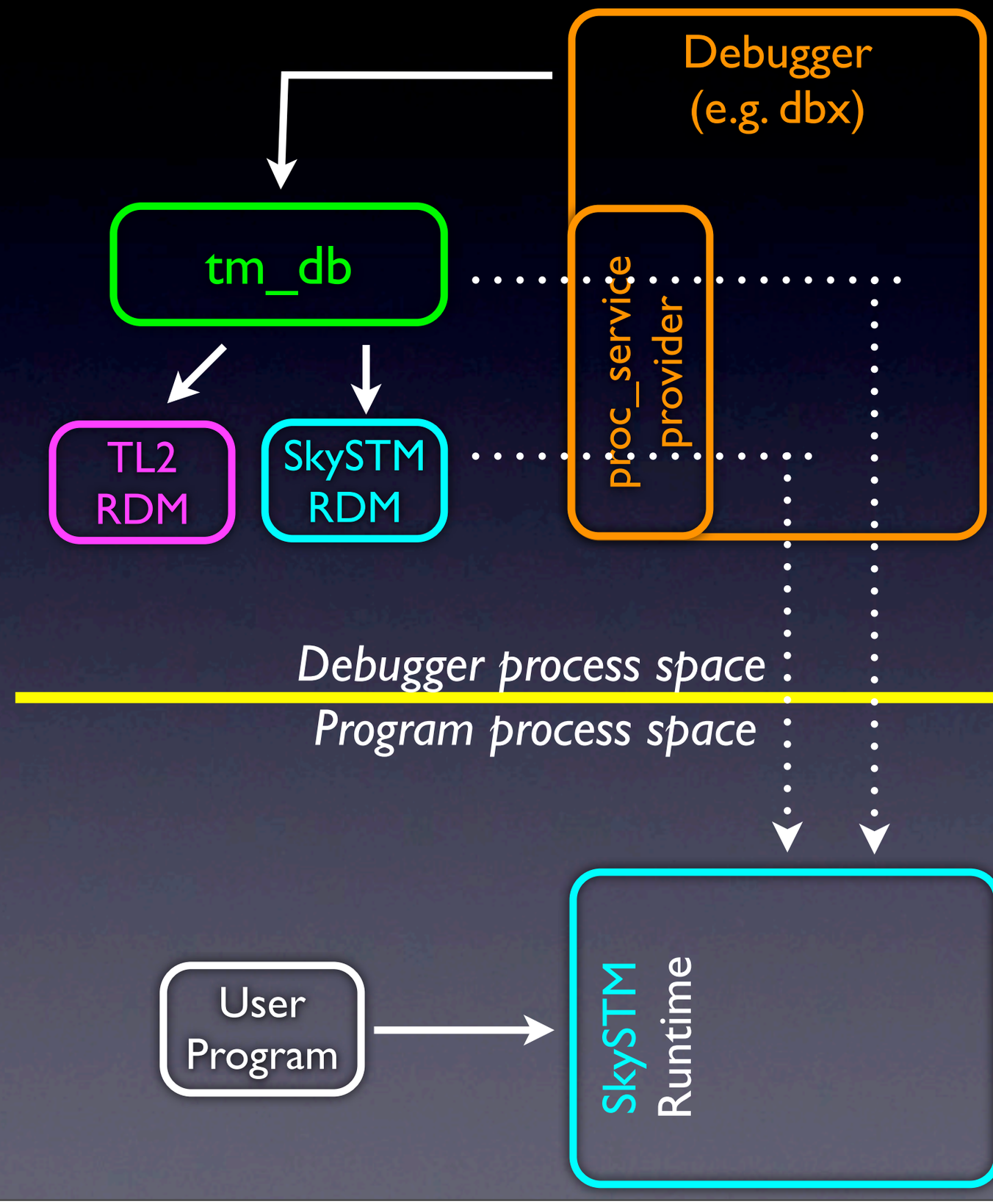

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  - But debuggers has to change
- We define an interface for generic transactional debugging support
- Provide a library that implements it
- Opens the door for transactional debugging support with various debuggers and runtimes